UNITED STATES SECURITIES AND EXCHANGE COMMISSION

Washington, D.C. 20549

FORM 8-K

CURRENT REPORT

Pursuant to Section 13 or 15(d) of The Securities Exchange Act of 1934 March 9, 2020

Date of Report (date of earliest event reported)



MICRON TECHNOLOGY, INC.

(Exact name of registrant as specified in its charter)

		1-10658	75-1618004
(State or other jurisdiction of incorporation)		(Commission File Numb	er) (I.R.S. Employer Identification No.)
		8000 South Federal Wa	ay
		Boise, Idaho 83716-963	32
	(A	Address of principal executive	e offices)
		(208) 368-4000	
	(Registra	ant's telephone number, includ	ding area code)
he following provisions (see General Written communications pursuant Soliciting material pursuant to Rule Pre-commencement communicatio Pre-commencement communicatio Securities registered pursuant to Sec	I Instruction A.2. to Rule 425 und e 14a-12 under to ons pursuant to ons pursuant to ction 12(b) of the	below): er the Securities Act (17 CFR he Exchange Act (17 CFR 24 Rule 14d-2(b) under the Exch Rule 13e-4(c) under the Exch Act:	40.14a-12) nange Act (17 CFR 240.14d-2(b) nange Act (17 CFR 240.13e-4c))
Title of each class		Trading symbol	Name of each exchange on which registered
0	LU per snare	MU	NASDAQ Global Select Market
Common Stock, par value \$0.1			
ndicate by check mark whether the r of this chapter) or Rule 12b-2 of the S			defined in Rule 405 of the Securities Act of 1933 (§230.405 of this chapter).
ndicate by check mark whether the r of this chapter) or Rule 12b-2 of the S Emerging growth company \square	Securities Excha	inge Act of 1934 (§240.12b-2 ark if the registrant has electe	of this chapter). ed not to use the extended transition period for complying

Item 1.02. Termination of a Material Definitive Agreement.

On March 9, 2020, Micron Technology, Inc. ("Micron") and Intel Corporation ("Intel") agreed to terminate, effective March 6, 2020, the Product Supply Agreement, dated April 6, 2012, among Micron, Intel, and Micron Semiconductor Asia Pte. Ltd. (the "PSA"). The PSA set forth the terms under which Micron agreed to supply 3D XPoint wafers to Intel at prices determined under a defined formula and in accordance with a negotiated forecast. Contemporaneous with the PSA's termination, Micron and Intel entered into a new 3D XPoint wafer supply agreement with changes to pricing and forecast terms. The new agreement is not material to Micron and does not change Micron's previously-communicated outlook that underutilization charges associated with Micron's Lehi, Utah fab will average approximately \$150 million on a quarterly basis in fiscal year 2020.

SIGNATURE

Pursuant to the requirements of the Securities Exchange Act of 1934, the Registrant has duly caused this report to be signed on its behalf by the undersigned, hereunto duly authorized.

Micron Technology, Inc.

Date: March 12, 2020 By: /s/ David A. Zinsner

Name: David A. Zinsner

Title: Senior Vice President and Chief Financial Officer